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Internship Details

The details presented in this section pertain to the VIII Semester activities of the Department of Electronics and Communication Engineering (ECE) for the Academic Year 2025–2026, including internship, project work, and other academic requirements.

S.No	Roll Number	Name of the student	Name of the internship organization/Company	Address of the internship organization	Duration of internship (in months)	Internship Learning Experience- Tools and Technologies used	Learning outcomes - Skills Developed	Overall Internship Experience	Stipend per month for internship
1	22761A0401	Abburu Sowjanya	CODE Technologies	Hyderabad	4	ARM	ARM	5	NA
2	22761A0402	Akkineni Jagadeesh	Nyerass	Bangalore	3	VLSI	VLSI	5	No
3	22761A0403	Akuthota Dharma teja	KT Semicon	Hyderabad	1.5	Xilinx vivado, xilinx vitis.	RTL DESIGN AND VERIFICATION	5	N/A
4	22761A0405	Bande Bhavya Sri	Taras Systems and Solutions	Chennai	6	EDA Playground	Gained knowledge on verilog and system verilog	4	N/A
5	22761A0406	Battula Devi	One stop	Hyderabad	4	Machine learning	Acquired knowledge on machine	3	Na

							learning		
6	22761A0407	Berothula Durgarao	SkillDzire	Hyderabad	3	VLSI design and chip architecture	VLSI	5	NA
7	22761A0408	Berothula Srihari	App Genesis	Vijayawada	4	Java	Java Developer	4	No stipend
8	22761A0409	B. Nithin	Nyerar	Bangalore	2	VLSI DESIGNER	VLSI	5	19500
9	22761A0410	Bhukya Gethan Kumar Naik	Triaright	Hyderabad	3	Data Science	Data Science	5	NO
10	22761A0411	Boddapati supriya	App genesis soft solutions private limited	Vijayawada	4	Java eclipse 2025	Springboot	5	N/A
11	22761A0412	HARSHITHA BOPPANA	NYERAS	Bangalore	4	EDA,software	Enabling, Desingning	1	Paid
12	22761A0413	Chillapalli Rupadevi	App genesis soft solutions pvt Ltd	Vijayawada	4	Java eclipse 2025	Springboot	5	N/A
13	22761A0414	Desu Veeranjanyulu	KT Semicon	Hyderabad	1.5	Xilinx vivado,xilinx vitis	RTL design and verification	5	NA
14	22761A0415	Srilatha Devireddy	Codec	Hyderabad	4	Snowflake	Sql analyst	4	N/A
15	22761A0416	Dirisinala Dharani	Codec technologies	Hyderabad	4	Arm	Arm	4	NA
16	22761A0417	D Venkata Siva Reddy	Nyerar	Bangalore	4	VLSI DESIGN	Design and simulation	5	No
17	22761A0418	G.Jyothika Achyutha	Tesslove	Bangalore	6	VLSI	VLSI project	4	No
18	22761A0419	Gorantla Akhil	Taras systems and solutions	Chennai	6	EDA Playground	VLSI design	5	N/A
19	22761A0420	Grandhi Sai Naga Lakshmi Srinivas	Taras system and solutions	Bangalore	6	VLSI design and verification	Coding	4	No
20	22761A0421	Sasi Gummadidala	Tessolve	Bangalore	6	Very good, scripting language (TCL)	Scripting language	5	15000
21	22761A0422	James Guntur	Nyerar	Bangalore AECS Layout	3	Cadence inovys	Physical design basics	4	N/A
22	22761A0423	Kandukuri	Sanmina	Orgadam	3	Surface Mount	During the	5	20000

		Prasanna kumar		,tamilnadu		Technology (SMT): Utilizes high-speed Fuji NXT SMT lines for PCB assembly (PCBA). Soldering Technologies: RoHS-compliant lead-free soldering, nitrogen-purged reflow, selective wave soldering, and hand soldering.	training, I gained knowledge of Surface Mount Technology (SMT), Fuji NXT assembly lines, IPC standards, and quality systems used in electronics manufacturing..		
23	22761A0424	Kati reddy Naga Jyothi	Sanmina	Vellakottai, chennai	6	Surface Mount Technology (SMT): Utilizes high-speed Fuji NXT SMT lines for PCB assembly (PCBA)	Technical & Engineering Skills SMT Operations & Knowledge: Comprehensive proficiency in Surface Mount Technology,.	5	20000
24	22761A0425	Kollabathina Bhavitha	Taras systems and solutions	Bangalore	6	EDA Playground	Verilog and system verilog	5	N/A
25	22761A0426	kottakotasreen ivasarao	Triaright	Hyderabad	2	Good	Numoy, pandas. Matlipplot, ml models	5	N/A
26	22761A0427	PAVAN KUMAR KOYYAN	Nyer as edu-tech and innovations Pvt Ltd	Bangalore	3	Machine learning	Industry practices	4	19.5k
27	22761A0428	Ajay kumar kuncham	Nyer as edo-tech and innovations pvt ltd	Bangalore	3	Cadence	Frontend in VLSI	5	No
28	22761A0429	Lingala Rajesh	Nyer as	Bangalore	2	It's very useful	VLSI design	5	N/A
29	22761A0430	Lingala Rajesh	Nyer as	Bangalore	2	It's very useful	VLSI design	5	N/A

30	22761A0431	Mamuduru venkata Deepak	Nyerass	Hyderabad	3	VLSI	Design and simulation	4	No
31	22761A0432	Mandadapu Akhila	Taras Systems and solutions	Bangalore	6	EDA Playground	Verilog and system verilog	5	N/A
32	22751A0433	Mandadi manoj	Nyerass	Bangalore	3	VLSI design	VLSI design learning	4	No
33	22761A0434	Mandala John Daniel	Nyerass edu-tech and innovations Pvt Ltd	Bangalore	3	Cadence	Front-end in vlsi	5	N/A
34	22761A0435	Marabathula Suresh	Tirairight the new era of learning	Ameerpet Hyderabad	2	Machine learning	Good	5	No
35	22761A0436	M Keerthi	TARAS SYSTEMS AND SOLUTIONS	Bangalore	6	EEDA Playground	Learnt verilog and system verilog	5	N/A
36	22761A0437	Modugu Pradeep	Nyerass edu-tech and innovations Pvt.lmt	Bangalore	3	Cadence	Front end in VLSI	4	No
37	22761A0438	Muppiri Kavva Sri	Codec technologies	Hyderabad	4	ARM	Arm	4	NA
38	22761A0439	Nagalla Venkata Krishna	Triaright	Triaright	2	Experience was good.tools numpy pandas matplotlib seaborn machine learning model	Developing models of machine learning using data	5	N/A
39	22761A0441	Pushpa lavanya paladugula	Nethra coding solution	Vijayawada	4	IoT	How to implement the real world application	5	No
40	22761A0442	Palaparthi Ramya Sri	Nethra Coding Solutions	Vijayawada	4	Good	Developed the real world applications	5	No
41	22761A0443	Pamula Sri Harsha	Nyerass Edu-tech And Innovations pvt .ltd	Bangalore	3	cadence virtuoso	schematic design , simulation results , layout design	5	12000
42	22761A0444	P.AJAY	Blackbuck	Blackbuck	6	Visual studio tools	creating	4	No

			engineers pvt ltd	engineers pvt ltd Hyderabad		and git bocker	complete, functional		
43	22761A0445	Pondugula Hemanthika	Taras systems abd solutions	Chennai	6	VLSI	Gaining knowledge on verilog system verilog	5	Na
44	22761A0446	Ramisetti Dhansi Dakshitha	1stop	Hyderabad	4	Machine Learning algorithm Google colab	Developed skills in data preprocessing, feature engineering, machine learning model	5	N/A
45	22761A0447	RANIMEKALA SIVAJI VENKATA GANESH	NYERAS	Bangalore	2	cadence	VLSI DESIGN	1	N/A
46	22761A0448	Seelam Sowmya	SkilDzire	APSCHE	3	VLSI and chip architecture	Project development	5	No
47	22761A0449	S.ShakthiRam	Nyerass	Bangalore	3	Cadence	Physical Design	5	N/A
48	22761A0450	Shaik afsar	Nyerass	Bangalore	2	Ardiuno	More knowledge on ardino	3	N/A
49	22761A0451	Shaik Kyrunbee	Datavalley India Pvt Ltd	Hyderabad	3	Java, springboot	Java full stack	4	No
50	22761A0452	Shek Vaheedha	Datavalley	Hyderabad	4	Software	Foundation syntax to complex	2	No
51	22761A0453	S.A,S,Harsha	1Stop	Hyderabad	4	Machine Learning algorithm Google colab	Developed skills in data preprocessing, feature engineering, machine learning model	5	N/A
52	22761A0454	S Bharat simha reddy	Suretrust	AICTE online	6	Espidf,baremethod coding,stm32	Bare method coding,stm32 protocol	4	Na

							projects, bluetooth and wifi protocols		
53	22761A0455	Telaprolu Sai Harshitha	Taras systems and solutions	Chennai	6	EDA Playground	Veri log, system verilog,uvm	4	N/A
54	22761A0456	T,Sushma	1Stop	Hyderabad	4	Machine Learning algorithm Google colab	Developed skills in data preprocessing, feature engineering, machine learning model	5	N/A
55	22761A0457	Thota Parasuram	Taras Systems and Solutions Private Ltd	Chennai	6	EDA Playground	Verilog, System verilog and UVM	5	N/A
56	22761A0458	TUMULURI BAVANA LAKSHMI DURGA	Tessolve	Chennai	6	EDA Playground	Got knowledge in VLSI, usage of EDA playground	5	NA
57	22761A0459	Bhavana	1 stop	Bangalore,	4	machine learning tools	machine learning and its algorithm	5	N/A
58	22761A0460	V. Chakradhar	Nyres	Hyderabad	3	VLSI	Vlsi designing	5	No
59	22761A0461	Veesam Geetha Narayanamma	Nyeras	Bangalore	2	Cadence	VLSI	3	NA
60	22761A0462	Vemula Navakumar	Triaright	Hyderabad	3	Machine learning	Industry practices	4	10k
61	22761A0463	Vepuri Jagadeesh babu	TRIARIGHT	Hyderabad	2	Machine learning tools	Machine learning algorithms	5	No stipend
62	22761A0464	Yadamakanti Ramya	Skill drize	Hyderabad	3	Cadence	Learning VLSI core concepts and implementation	4	No
63	22761A0465	Y.Yogendra venkata sai	Taras	Chennai	6	VLSI design and verification	VLSI design and verification	5	N/A
64	22761A0466	Y KANAKA DURGA	DEXTERITY	Bangalore	3	cadence tools	gained knowledge in	3	N/A

							using cadence tools		
65	22761A0467	A. Bhavaswitha	Welcord components pvt. lmt	Mettupalayam	4	PCB design	Pcb design	5	No
66	22761A0468	B.keerti	Apex planet software pvt ltd	Hyderabad	3	Good	Full stack development	5	Na
67	22761A0469	BANAVATHU CHINNI	Taras systems and solutions	Chennai, Tamilnadu	6	EDA Playground	Basic Command on hardware languages	5	N/A
68	22761A0470	Battula thirupathi rao	Datavalley india pvt. Ltd	Hyderabad	4	Mechine learning	Data science	5	No
69	22761A0471	B srinadh	Tessolve	Bangalore	6	Tessent	Dft	5	15k
70	22761A0472	BODDU SANGEETHA	PTRON	Hyderabad	3	Development of Product	Quality check, Assembling, Operating	4	NA
71	22761A0473	Boddu snehalatha	Tanasvi technologies pvt	Visakhapatnam	4	Cadence	Rtl design	4	No
72	22761A0474	B.Gopi Krishna	CSC india	Vijayawda	44	Software Programmer Python		5	N/A
73	22761A0475	Boggula Upendar Reddy	Datavalley	Hyderabad	4	Data science	Algorithm,tools, web	5	No
74	22761A0476	Bondalapati Devika	Taras	Chennai	6	EDA Playground	I learned the tool EDA	5	NA
75	22761A0477	Mokshagna7 Boppudi	Datavalley	Vijayawada	4	8086,gbd	hands on experience in some tools	4	N/A
76	22761A0479	Rohitha Chede	Soft Nexis Technology	Bangalore	2	Visual studio	Java	5	N/A
77	22761A0480	CHOPPARA INDRA KUMAR	Ptron	Hyderabad	3	Development of product	Quality check , assembling, operating	4	NA
78	22761A0481	Rohith	KT Semicon	Hyderabad	3	RTL Design and FPGA	RTL Design and FPGA	5	No
79	22761A0482	Dasari Adi	Tanasvi	Visakhapatnam	4	Cadence	Rtl design	4	No

		lakshmi	technologies Pvt						
80	22761A0483	D. Ashok Kumar	TARAS SYSTEMS AND SOLUTIONS	Chennai, Tamil Nadu	6	EDA Tool	Basic Command on Hardware Languages	4	N/A
81	22761A0484	D. Ashok Kumar	TARAS SYSTEMS AND SOLUTIONS	Chennai, Tamil Nadu	6	EDA Tool	Basic Command on Hardware Languages	4	N/A
82	22761A0485	E.Vinay venu gopal	SkillDzire	Andhra Pradesh	3	VLSI and chip architecture	Project development	5	No
83	22761A0486	Eppili Pravalika	Parled Electronics	Hyderabad	3	Printed Circuit Board, Quality Checking, Shouldering components	Very well experience in electronics sector	5	No
84	22761A0487	Gaddala Gopi	Nyeras	Hyderabad	6	Cadence	Design	5	Paid
85	22761A0488	Gosu Lavanya	Tanasvi technologies Pvt Ltd	Vishakhapatnam	4	Cadence	RTL design	4	N/A
86	22761A0489	G Alekhya	Skilldzire	Hyderabad	4	Vivado	Advance tools and technologies	4	NA
87	22761A0490	Gujjula Tirupati Reddy	Skilldzire	Hyderabad	4	Vivado	Verilog	5	No
88	22761A0491	J Vamsi	Skill Dzire	Hyderabad	3	VLSI design and chip architecture	VLSI	5	N/A
89	22761A0492	JAVISETTI VEERANJI	Skilldzire	Hyderabad	3	Cadence	Strong foundation in cadence tool	5	N/A
90	22761A0493	J. KUSHWANTH REDDY	Taras Systems and Solutions	Chennai	6	EDA Playground	Learned verilog and system verilog and eda tools	5	No
91	22761A0494	Kadavakollu Kiran Deepthi	Taras systems and solutions	Chennai	6	EDA Playground	Verilog, system verilog, uvm	5	Na
92	22761A0495	Katuri Hepsiba	Nit Warangal	Warangal	6	Good, deep learning	Machine learning	5	8000

93	22761A0496	Kode Salomi Naidu	Ptron electronics	Hyderabad	3	Development of a product, quality check , assembling	Development of a product	4	NA
94	22761A0497	KONIJETI NAGAJYOTHI	SkillDzire	Hyderabad	4	VLSI and Chip Technology	Verilog HDL	5	N/A
95	22761A0498	Susmitha kuppireddy	SkillDzire	Hyderabad	4	SQL Server	SQL	4	NA
96	22761A0499	Kuthada Tirupathamma	NCS(Nethra coding solutions)	Vijayawada	4	IOT	IoT	3	N/A
97	22761A04A0	Madala susmitha	Taras systems and solutions	Chennai	6	Verilog and sv and uvm	VLSI design and verification	4	No
98	22761A04A1	MADU NITYA NAVEEN	VLSI design and Chip Architecture	JNTU Kakinada	3	Vivado xilinx	Chip designing	4	N/A
99	22761A04A2	Maheswara Varshini	Skill Dzire	Hyderabad	4	8019692530	Embedded systems	4	NA
100	22761A04A3	Meda Nithin Chowdary	Cuberaiges IT solutions	Hyderabad	2	VLSI	VLSI	5	N/A
101	22761A04A4	MD MEERAZ JANI	SkillDzire	Hyderabad	4	Chip architecture tool	Chip integration	5	N/A
102	22761A04A5	Nagula Manish	Data valley	Hyderabad	4	Python and sql	Understand theoretically and technically the steps of a general data science process.	5	N/A
103	22761A04A7	Parisa Gowtham kumar	SkillDzire	Hyderabad	4	Vivado	Verilog, system verilog	5	No
104	22761A04A8	Peram Avinash Reddy	App Genisis	Vijayawada	4	Frontend(HTML, CSS, javascript), Backend(Django, Flask), DBMS(Sql, Nosql)	Development, Debugging	4	N/A
105	22761A04A9	POLAMREDDY TULASI MADHU LATHA	Codec Technologies Pvt.Ltd.	Chennai	4	Embedded systems	Embedded systems	4	N/A

106	22761A04B0	POTLAPALLI RUDRA REDDY	KT semicon	Hyderabad	4	23 th Jan 2026 -30 th April 2026	Learning project	2	Na
107	22761A04B1	Prathipati Mounika	Prime vector	Bangalore	3	Good	Yes	5	19000
108	227610A4B2	P.Divya	Dynamix networks	Gurgaon, Haranya	4	Data Analyst	Data Analyst	4	Na
109	22762A04B3	RAMISETTY KEERTHI SAI SANJANA	KT SEMICON	HYDERABAD	3	FPGA	FPGA TOOLS	5	N/A
110	22761A04B4	RAVURI JAMALIAH	TRIARIGHT the new era of learning	Hyderabad	3	Machine learning	Machine learning and python	3	NA
111	22762A04B5	RAMISETTY KEERTHI SAI SANJANA	KT SEMICON	HYDERABAD	3	FPGA	FPGA TOOLS	5	N/A
112	22761A04B6	Seelam Achireddy	Python Full stack development	Data valley	3	Python,html CSS,sql	Web pages	2	NA
113	22761A0B7	SHAIK FAIZAN	Prime vector	Hosur, banglore	3	Good	No need	5	19000
114	22761A04B8	SUKKA AVINASH	SKILL DZIRE	Hyderabad,	4	Good	Cadence	4	No
115	22761A04B9	S. Kasi Visweswar Reddy	Skill dizire	Hyderabad	3	Good	VLSI design	4	N/A
116	22761A04C0	Tadiboina Lakshmi Sravani	Taras Systems & Solutions	Chennai, Tamil Nadu	6	EDA Playground, VS code, GTK Wave, VIVADO	understood what actually VLSI DV does and how the codes are written and verified. VLSI Flow and got handson experience with tools and learned about Design	5	NA

							verification, System verilog, UVM, verilog		
117	22761A04C1	Tadikamalla Siddhartha	Inndata analytics	vizag	2	Machine learning with python	Learnt pyhton	4	N/A
118	22761A04C2	Tallapureddy Aravinda Maheswara Reddy	CODTECH IT SOLUTIONS PRIVATE LIMITED	Hyderabad	4	Cadence	Learned how to do projects in cadence tool	4	N/A
119	22761A04C3	T. Harshitha	CODTECH IT SOLUTIONS PRIVATE LIMITED	Hyderabad	4	Cadence	Learn how to do project	5	No
120	22761A04C4	Thanneeru Naveen	VLSI design and chip architecture	Hyderabad	4	Cadence	Learned cadence tool	4	N
121	22761A04C5	Thota Jyothika	Codtech it solutions Pvt Ltd	Hyderabad	4	Cadence	A strong confidence to how to use cadence tool and how to complete projects in the tool	5	NA
122	22761A04C6	Tulluri.Srija	CODTECH IT SOLUTIONS PRIVATE LIMITED	Hyderabad	4	Cadence	Learn how to do project by using the tools	5	No
123	22761A04C7	TUMMA VAMSI	ANJANAAS FOUNDATION	HYDERABAD	2	.	.	4	N/A
124	22761A04C8	V.Sahithi	SAMEER CE3	Visakhapatnam	6	CST	CST,EMI/EMC	5	12,300
125	22761A04C9	Varikuti Sainath	Triaright	Hyderabad	3	Machine learning	MI	3	No
126	22761A04D0	VEMIREDDY SRINATHRED DY	Council for skills and competencies	Andhara pradesh state council of higher education	4	python	Python language	4	NA
127	22761A04D1	Y.jagadesh	Taras systems and solutions	Chennai	6	EDA Playground	Learned uvm ,verilog , system verilog	5	N/A

128	22761A04D2	Y.Rama Lakshmi	Taras Systems& Solutions	Chennai	6	VLSI	VLSI	5	N/A
129	22761A04D3	A.Ambica sri lasya	Taras Systems and Solutions	Bangalore	6	Verilog HDL, SystemVerilog, EDA Playground, RTL Design, Digital Design Concepts.	RTL design using Verilog, SystemVerilog testbench development, functional verification, debugging digital circuits.	5	No
130	22761A04D5	Bandi Navadeep Reddy	SCHAEFFLER INDIA LIMITED	Bangalore	3	CNC machine's	To understand the CNC machine's	5	18000
131	22761A04D6	BAPATHI CHENNA KESAVA REDDY	Taras system and solutions	Chennai	6	EDA Playground	Digital design	4	4000
132	22761A04D7	B.Gayathri	Bharat Electronics limited	Bangalore	45	Good	About PCB designing	5	No
133	22761A04D8	BHOGYAM LOKESWAR	TARAS SYSTEMS AND SOLUTIONS	Chennai	6	EDA Playground	Verilog, system verilog and uvm basic	4	N/A
134	22761A04D9	BHUKYA BALAJI	SkillDzire	JNTU kakinada	3	Cadence verilog	Circuits designing	3	N/A
135	22761A04E0	B.Prasanthi	APP Genesis software solutions private limited	Chennai	4	Embedded Systems	C language	4	N/A
136	22761A04E1	Ch Hruday Chowdary	SkillDzire	Online	4	Cadence, x circuit	VLSI design	4	N/A
137	22761A04E2	Bhuvaneswar	Schaeffler India Limited	Bangalore	3	Rtygcjjvj	Understanding the real industry exposure	3	18000
138	22761A04E3	Chodagam Khyathi Sree	Council for skills and competencies (CSC India)	Hyderabad	5	Software programmer python	Python basics	4	NA
139	22761A04E4	Darapureddy Kalyan Kumar	Skill Dzire	Hyderabad	3	Raspberry pi pico	Doing projects	4	NA

140	22761A04E5	Dasari Abhitej	Council for skills and competencies(csc india)	JNTU kakinada	5	Software program Python	Python programming	4	No
141	22761A04E6	D.Sai Keerthi	APSHE	Mangalagiri	4	Machine learning and deep learning	Machine learning and artificial intelligence	3	No
142	22761A04E7	Davu V V N Gayathri	App Genesis	AP intership program	4	Embedded systems	Developed embedded project related	3	NA
143	22761A04E8	DEVISETTI GOPI	App Genesis Soft Solutions Pvt Ltd	Vijayawada	4	EC2,S3 tools are used	Basic understanding of server management and deployment	5	NA
144	22761A04E9	Garike Nancharamma	KT semicon	Hyderabad	3	Good and VLSI	Problem solving and Team work	4	NA
145	22761A04F0	G.Durga kavya	SkillDzire	Online	3	Cadence,X circuit	Analog circuit design	5	N/A
146	22761A04F1	G.Harshitha	CODEC Technologies	Hyderabad	4	ARM	ARM	4	NA
147	22761A04F2	G.lokesh	SkillDzire	JNTU kakinada	3	Cadence verilog	Circuits designing	3	N/A
148	22761A04F4	Kamkolam Gayathri	Taras systems and solutions	Chennai	6	EDA Playground	Verilog, system verilog , UVM	4	N/A
149	22761A04F5	Kanakala Pujitha	Anjanas Foundation	Hyderabad	3	Artificial intelligence	Ai	3	N/A
150	22761A04F6	KANCHARLA HEMA LATHA	Taras	Chennai	6	Cadence	I have learnt basics of Cadence and VLSI	5	Na
151	22761A04F7	Kanumuri Ritish Kumar	Anjanaas foundation	Machilipatnam	4	AI	BASICS OF AI	4	0
152	22761A04F8	Kollabathina Navitha	Nethra Coding Solutions	Online	4	Adriuno ide,thony	IoT	4	N/A
153	22761A04F9	K.Ajay Kumar	Tessolve/ taras solutions	Bengaluru	6	Xilinx Vivado	VLSI design	5	N/A

154	22761A04G0	KOTHAPALLI PALLAVI	TARAS SYSTEMS AND SOLUTIONS	Chennai.	6	good	good	5	N/A
155	22761A04G1	Bharath simha Reddy	SkillDzire	Ap	4	Cadence	Skills development	4	NA
156	22761A04G2	M.Sukanya	APSCHE	APSCHE, Vijayawada	4	Artificial intelligence	AI&ML	4	N/A
157	22761A04G3	MOGADATI KAVITHA	Taras and Solutions	Online, Chennai	6	Design verification VLSI	Design verification	5	Free
158	22761A04G4	M.Dileep Vamsi Reddy	APSCHE	APSCHE Vijayawada	4	AI & ML	Artificial intelligence	3	N/A
159	22761A04G5	M.Venkata Nithin	1Stop	Hyderabad	4	Machine Learning algorithm Google colab	Developed skills in data preprocessing, feature engineering, machine learning model	5	N/A
160	22761A04G6	Modugu Lasya Priya	Datavalley	Hyderabad	4	Good	Knowledge about embedded systems	5	N/A
161	22761A04G7	Mohammad Riyaz	Anjannas Foundation	Hyderabad	4	Ariti	Python and java	4	N/A
162	22761A04G8	Moka.Kusuma priya	Prime vector	Hosur, Tamil nadu,635109	3	Cadence,Xilinx vivado,CMOS technology,RTLdesign	Communication skills	5	N/A
163	22761A04H1	Mudikonda Harikrishna	App genesis soft solutions private limited	Chennai	4	Embedded systems	Teamwork	4	NA
164	22761A04H2	Mule Swathi	Taras systems and solutions	Bangalore	6	Cadence	VLSI and uvm basics	5	N/A
165	22761A04H3	M.Deepika	App genesis soft solutions private limited	Chennai	4	Embedded system	Embedded systems	3	N/A
166	22761A04H5	Odugu Hema Sai Sri	Taras systems and solutions	Bangalore	6	EDA Playground	Rtl design,verilog,s	4	N/A

		Chandana					ystem verilog		
167	22761A04H6	P varun teja	NYERAS EDU-TECH AND INNOVATIONS PVT. LTD	Bengaluru	3	EMBEDDED SYSTEMS	EMBADDED SYSTEMS	4	Na
168	22761A04H7	Prathi Leela Pravallika	App gensis soft solutions private limited	Chennai	4	Embedded system	Team work	4	NA
169	22761A04H8	Pulluru Srija	App gensis soft solutions private limited	Chennai	4	Embedded systems	C language	4	N/A
170	22761A04H9	Punugoti kalyani	App gensis soft solutions private limited	Chennai	4	Embedded systems	Problem solving	4	NA
171	22761A04I1	Teja Nuthan Rali	TARAS	Chennai	7	Verilog Cadence	proficiency in EDA tools	5	NA
172	22761A04I2	Bhargavi Sanepalli	Smart brige	Hyderabad	6	Google cloud gen AI	About the gen AI and its technology	4	N/A
173	22761A04I3	Anjana Sangana	APP GENESIS SOFT SOLUTIONS PVT.LTD	Vijayawada	4	Arduino (Embedded Technology) and IOT	Microcontrollers controlling	5	No
174	22761A04I4	Priyanka Sangepu	Servicenow university	Online	4	AI,Frameworks	Dsa frameworks	5	N/A
175	22761A04I5	Seelam venkat Narendra reddy	1Stop	Bangalore,	3	Machine learning	Gain knowledge on ML	5	N/A
176	22761A04I6	SK ASRA BEGUM	Anjanaas foundation	Hyderabad	2	Good experience	Learned about AI in detailed	4	NA
177	22761A04I7	Shaik Ruthik	SkillDzire	JNTU KAKINADA	3	VLSI design	Using cadence verilog tools	4	N/A
178	22761A04I8	Siddela manasa	Prime vector private limited	Hosur,tamil naidu, 635109	3	Neutral	Communication skills	2	N/A
179	22761A04I9	Tallapureddy Venkata Hemanth Kumar Reddy	Kia	Ananthapur	2	Learning	Super	5	Jan to march

180	22761A04J0	Phani vardhan	Nyerass	Bangalore	3	VLSI	VLSI	5	No
181	22761A04J1	Rahul Vallabhaneni	Anjanaas foundation	Hyderabad	2	AI TOOLS	AI	3	N/A
182	22761A04J2	VANAMA VANDHIK	NYERAS	Bangalore	3	Embedded systems	Embedded code	1	N/A
183	22761A04J3	Veeramalla jamalaiah	Apsche	Vijayawada	4	Ai and ml	Ai & ml	5	NA
184	22761A04J5	Veenamadhuri Vemuri	Taras systems and solutions	Chennai	6	Verilog,system verilog,UVM	Verilog,system verilog,UVM	4	N/A
185	22761A04J6	V.Anjusri	Taras	Bangalore	6	EDA Playground	Improving skills	5	No
186	22761A04J7	Yallala sreedhar reddy	Istop	Bangalore	5	Ai	NA	4	N/A
187	22761A04J8	Y.L.Narendra Babu	SkillDzire	Hyderabad	3	VLSI	VLSI	3	NA
188	23765A0401	Gopiseti kalyani	Codec technologies	Mumbai, Maharashtra	4	JavaScript,mongoDB,SQL	semi Full stack developer	4	NA
189	23765A0402	K Chaitanya Sree	CODEC Technologies	Mumbai, Maharashtra	4	Python	Python	4	N/A
190	23765A0403	Kommuri Rohith Vishnu	Nyerass	Bangalore	3	VLSI	Cadence	5	No
191	23765A0404	Mediseti Devi Bhavani	Codec Technologies	Mumbai, Maharashtra	4	Python	Python	3	N/A
192	23765A0405	Polagani Tarun	Triaright	Hyderabad	3	Data science	Data science	5	No
193	23765A0406	V. Tejeswar	Triaright	Hyderabad	2	Machine learning	Machine learning	4	No
194	23765A0407	Annabathula Sravani	Codec	Vijayawada	6	Software	Software	5	0 NA
195	23765A0408	G Ganesh Kumar	NIT Rourkela	Rourkela, odisha	2	Vivado	Good exposure of vivado tool accordance with research work	5	10,000
196	23765A0409	Golla Rajkumar	InnData Analytics	Visakhapatnam	4	Machine learning with python	Machine learning with python	4	N/A

197	23765A0410	GUDELA SNEHA LAHARI	SkillDzire	Hyderabad	4	Embedded Systems	Embedded Systems	4	N/A
198	23765A0411	Guntaka Sri Naga Durga	Tanasvi technologies Pvt Ltd	Visakhapatnam	4	GitHub,EDA Playground	VLSI,verilog	4	NA
199	23765A0413	A.Eswar	Skill dezire	Online	3	Cadence,Xcircuit	Analog circuit design.	5	N/A
200	23765A0414	Abhishikth Darsi	SkillDzire	Hyderabad	3	good	coding and learned how to use hardware	3	N/A
201	23765A0415	Jujjuvarapu sandhya	Vr siddartha(college)	Vijayawada	3	Linx	About VLSI	5	Nill
202	23765A0416	KARINKI SIVA GANESH	SkillDzire	Hyderabad	3	Arduino IDE	Embedded C	5	NA

HOD-ECE

	Company	Number of students
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	1stop	05
	Anjanas Foundation	06
	Apex planet software pvt ltd	01
	App genesis soft solutions private limited	13
	APSCHE	03
	Bharat Electronics limited	01
	Blackbuck engineers pvt ltd	01
	CODE Technologies	14
	Council for skills and competencies (CSC India)	03
	Cuberaiges IT solutions	01
	Datavalley India Pvt Ltd	07
	DEXTERITY	01
	Dynamix networks	01
	Inndata analytics	02
	Kia	01
	KT Semicon	06
	Nethra coding solution	04
	Nit Warangal	01
	NIT Rourkela	01
	Nyeras edu-tech and innovations Pvt Ltd	23
	Parled Electronics	01
	Prime vector	04
	PTRON	03

	Python Full stack development	01
	Sanmina	02
	SAMEER CE3	01
	SCHAEFFLER INDIA LIMITED	01
	Schaeffler India Limited	01
	Servicenow university	01
	SkillDzire	26
	Soft Nexis Technology	01
	Smart brige	01
	Suretrust	01
	Tanasvi technologies pvt	04
	Taras Systems and Solutions	30
	Tesslove	05
	Triaright	10
	VLSI design and Chip Architecture	02
	Welcord components pvt. lmt	01
	Vr siddartha(college)	01